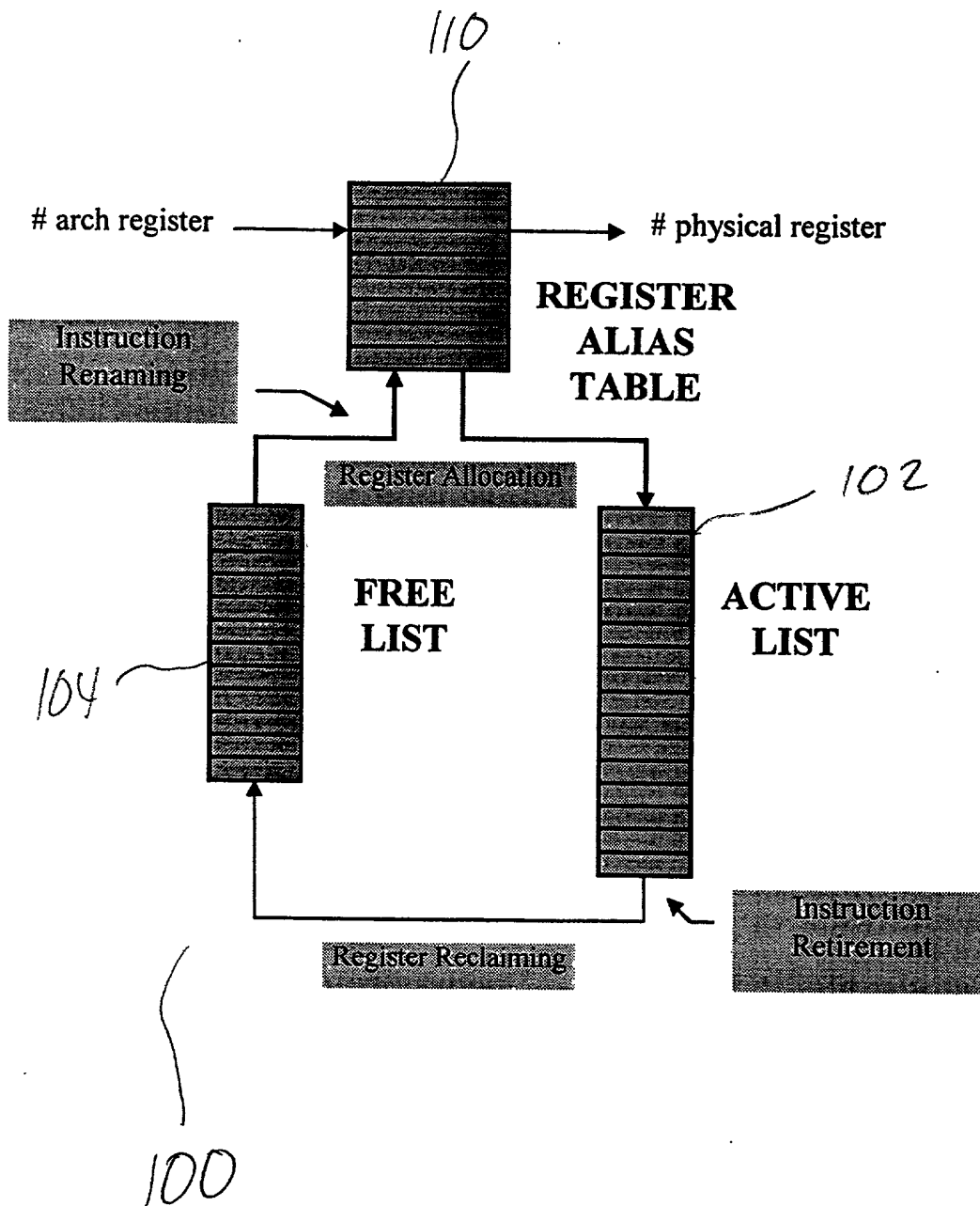


Fig. 1
PRIOR ART



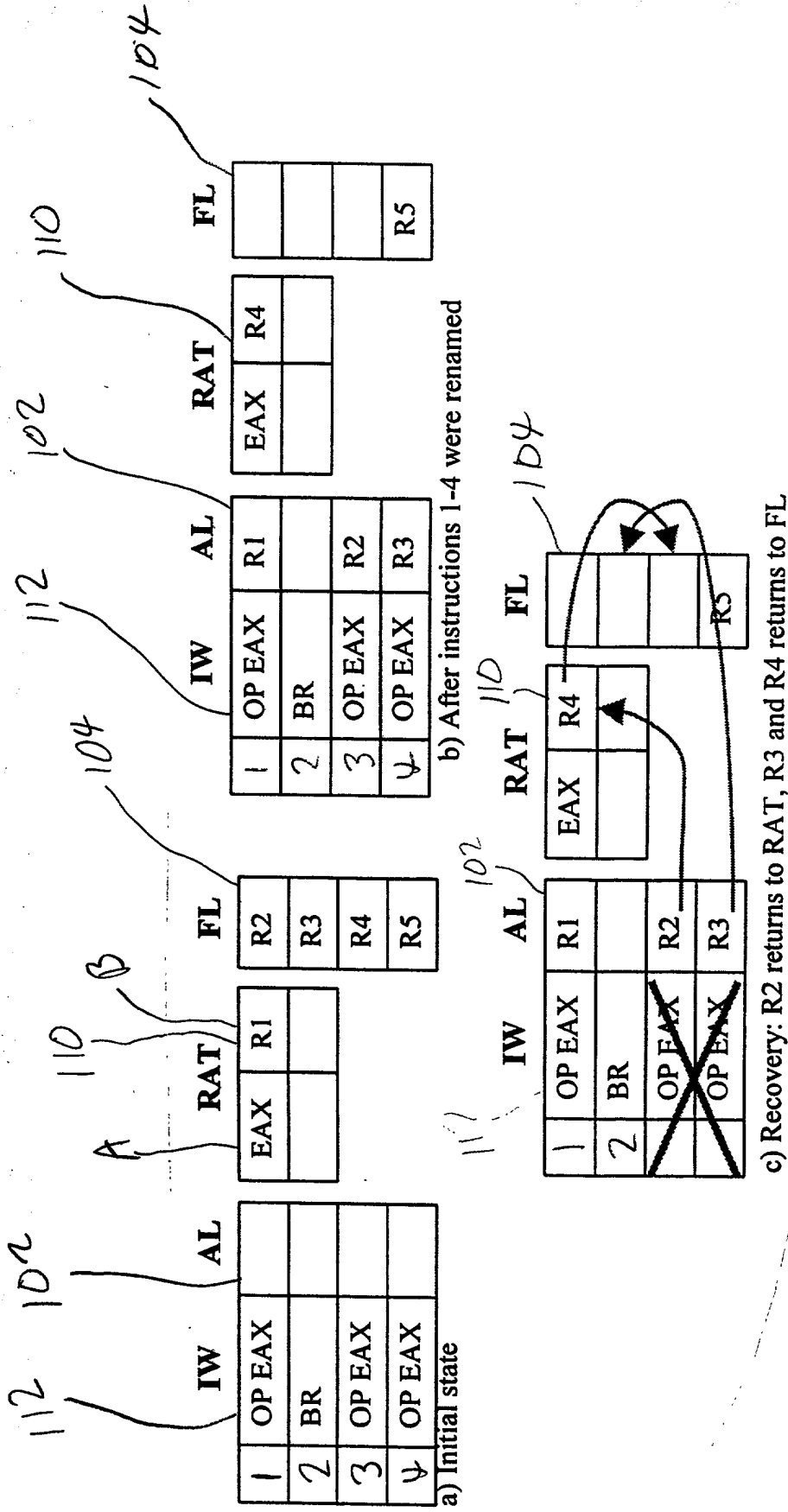


Fig. 2
PRIOR ART

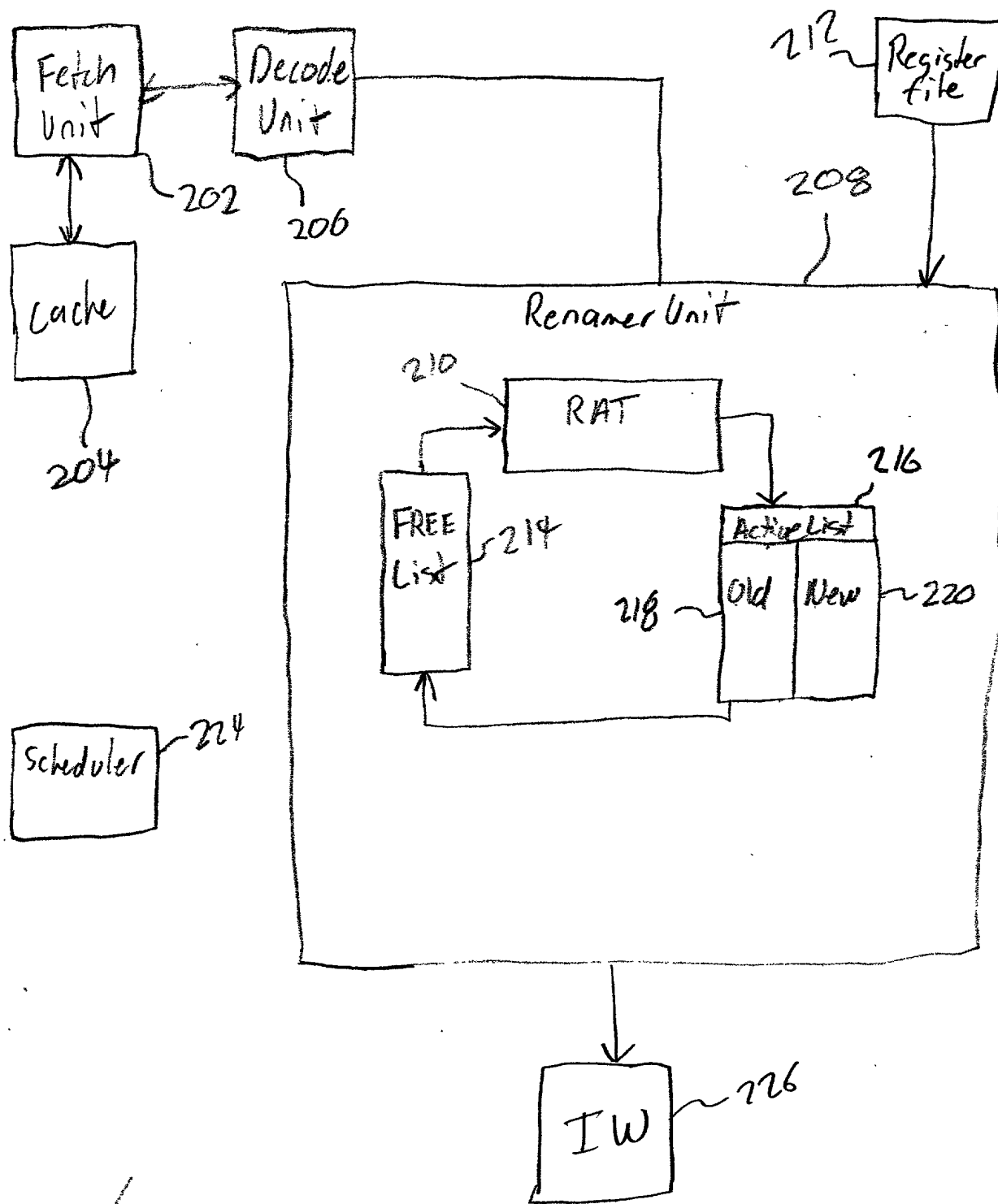
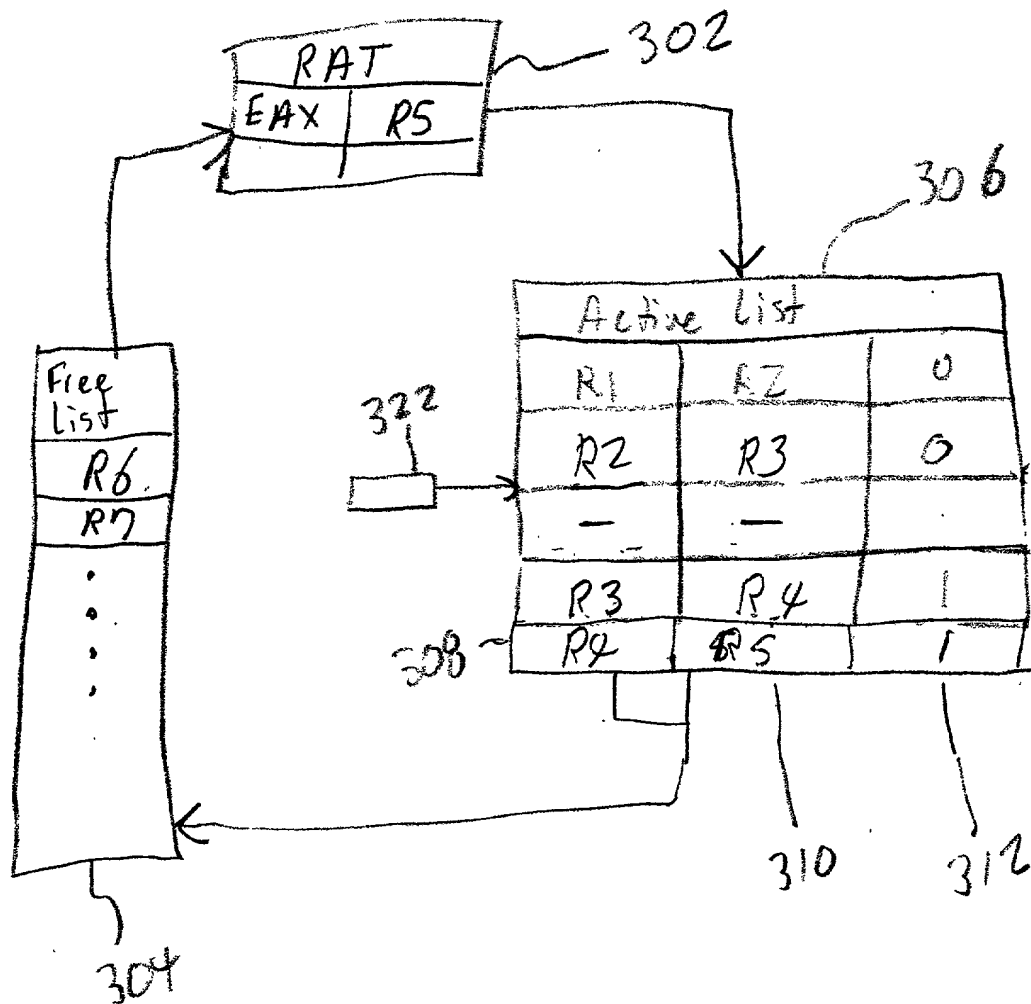


Fig. 3



Hand-drawn diagram illustrating a 5-entry instruction window (IW) and a 32-bit register.

The instruction window (IW) is a table with 5 rows and 3 columns:

Index	Instruction	Register
1	OPEAX	R1
2	OPEAX	R2
3	BR	
4	OPEAX	R3
5	OPEAX	R4

A 32-bit register is shown to the right of the IW, connected to the third slot (index 3) of the IW. The register is labeled "32" and has a box next to it.

Figs. 4

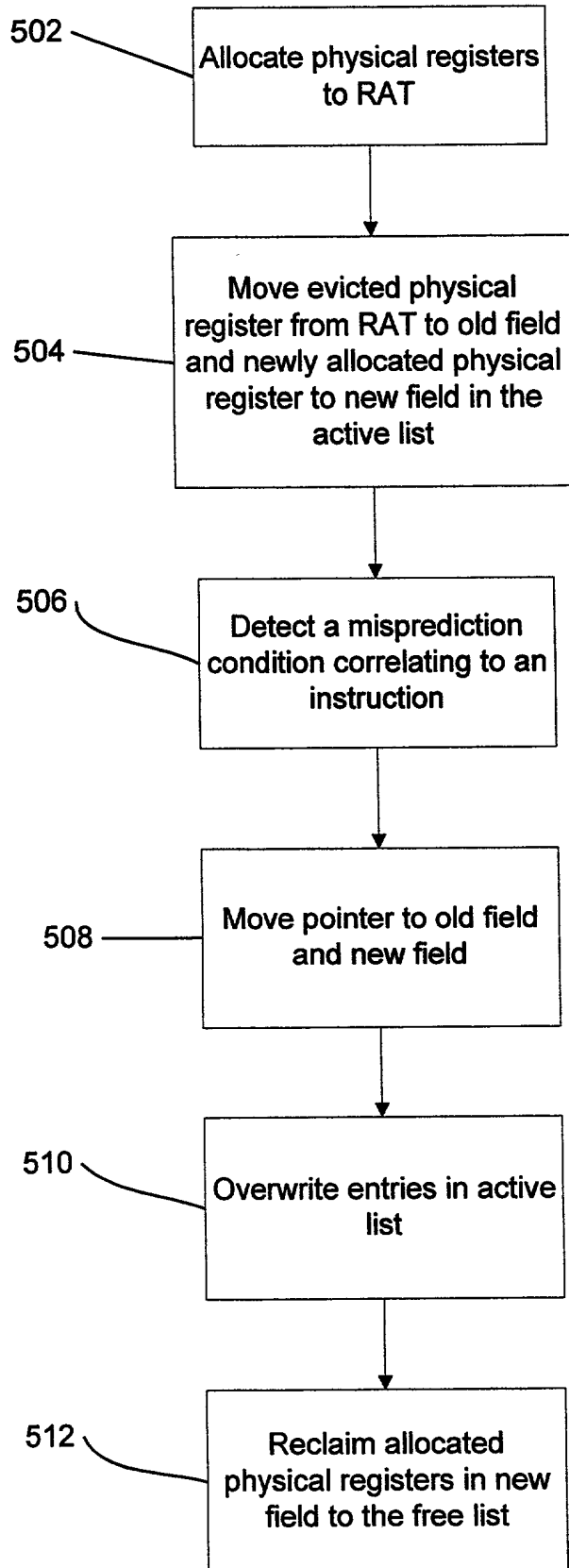


Fig. 5

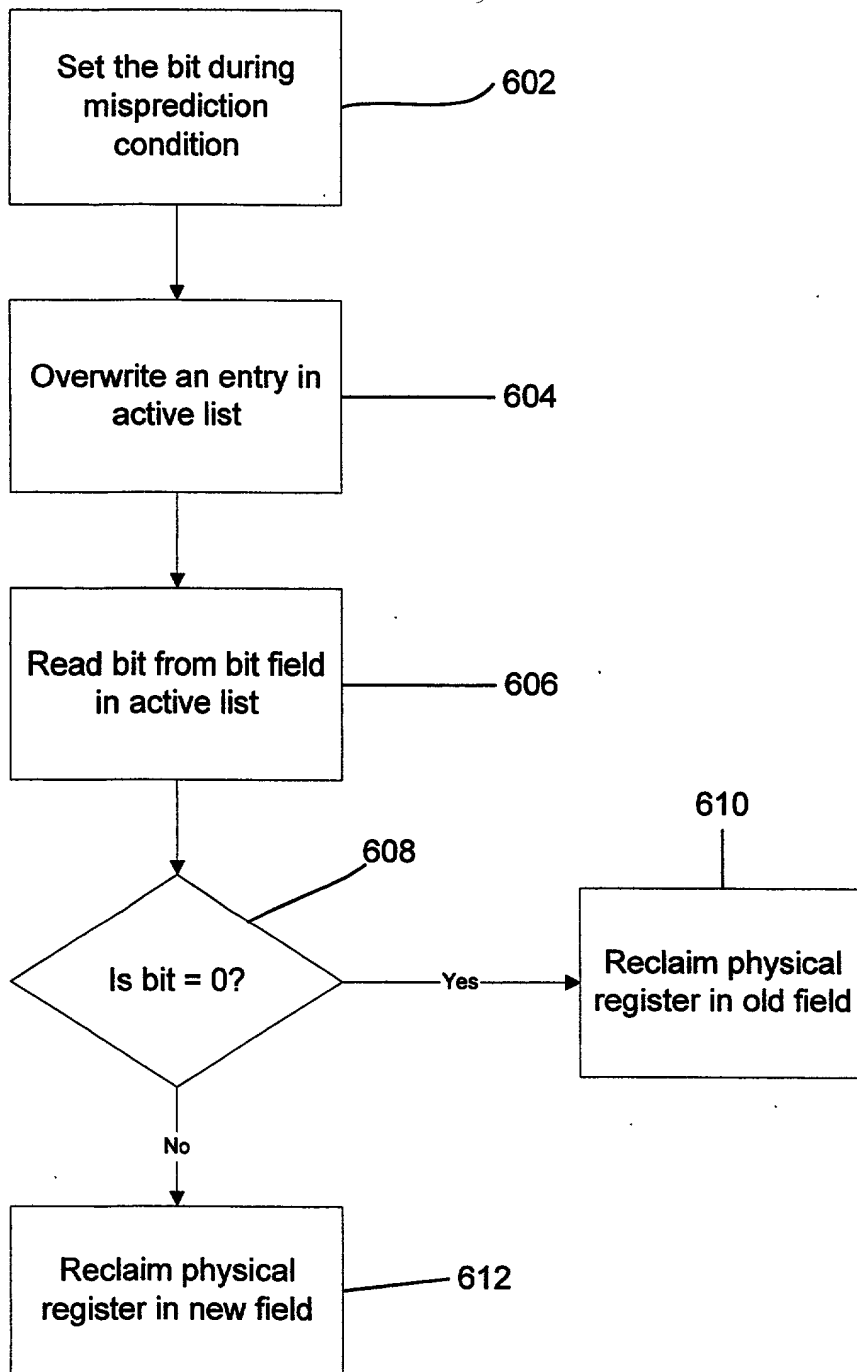


Fig. 6